



4-11-05

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EV 37 13008 10

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....10/618,471  
Confirmation No. ....5311  
Filing Date ..... July 11, 2003  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit .....2813  
Examiner ..... Laura M. Schillinger  
Attorney's Docket No. ....MI22-2331  
Customer No. ....021567  
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

**RESPONSE TO JANUARY 11, 2005 OFFICE ACTION**

To: Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**VIA EXPRESS MAIL**

From: Deepak Malhotra (Tel. 509-624-4276; Fax 509-838-3424)  
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Responsive to the Office Action dated January 11, 2005, Applicant amends and remarks as follows. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

**AMENDMENT**

**Amendments to the Claims** begin on Page 2 of this paper.

**Remarks** begin on Page 6 of this paper.